

Fig. 1

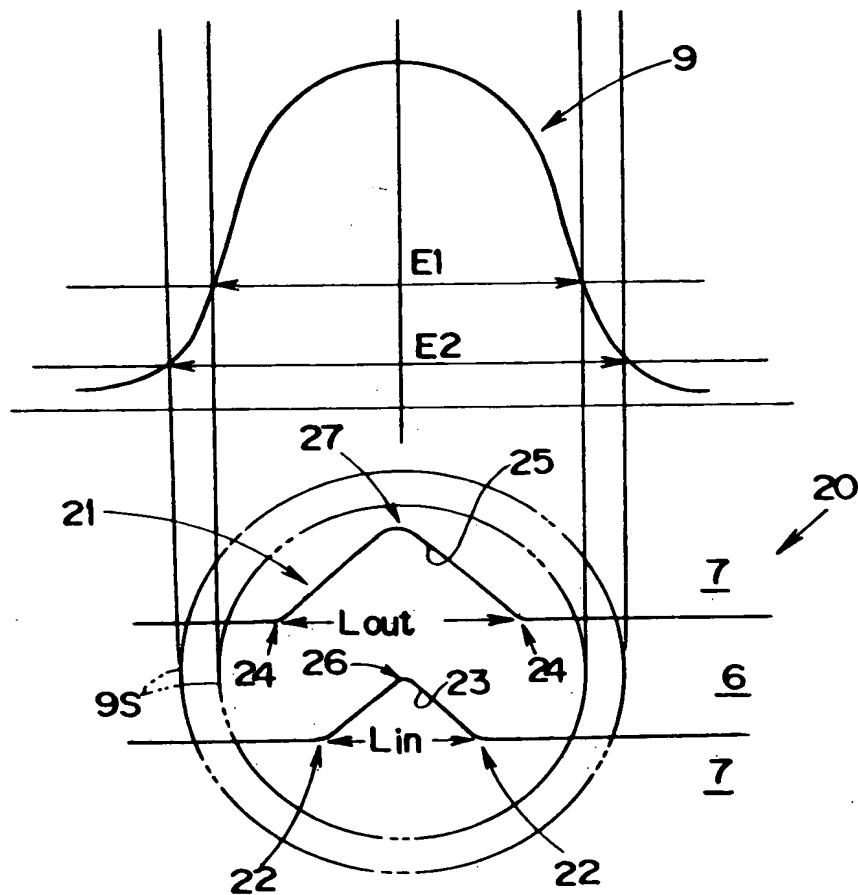
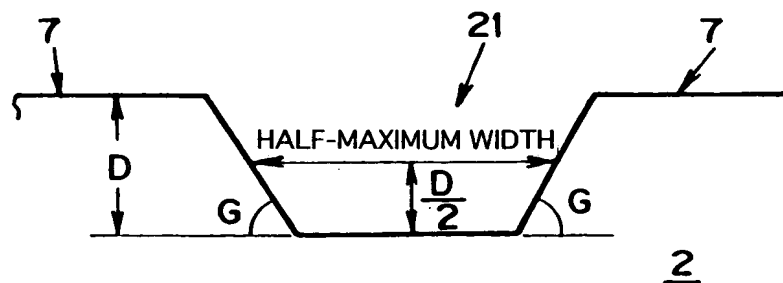


Fig. 2





$$\begin{pmatrix} 3T \leq L_{out} \leq 6T \\ 3T \leq L_{in} \leq 6T \end{pmatrix}$$

FIG. 1 is a schematic diagram of a circular device 7. A dashed line 9S(9) represents a boundary. Inside, a zigzag line 27 connects points 25 and 26. A horizontal line 21 passes through the center. Two horizontal lines 6 are shown below the center. Arrows labeled "Lout" and "Lin" point to the right and left respectively. Various other labels (22, 23, 24, 50) and underlined numbers (7, 6, 7) are present.

**Fig.10**

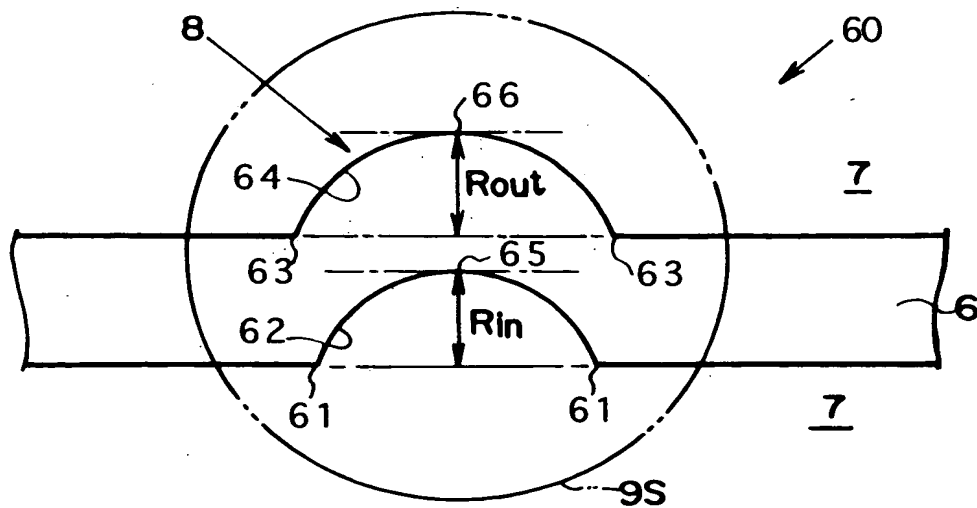


Fig.1 1

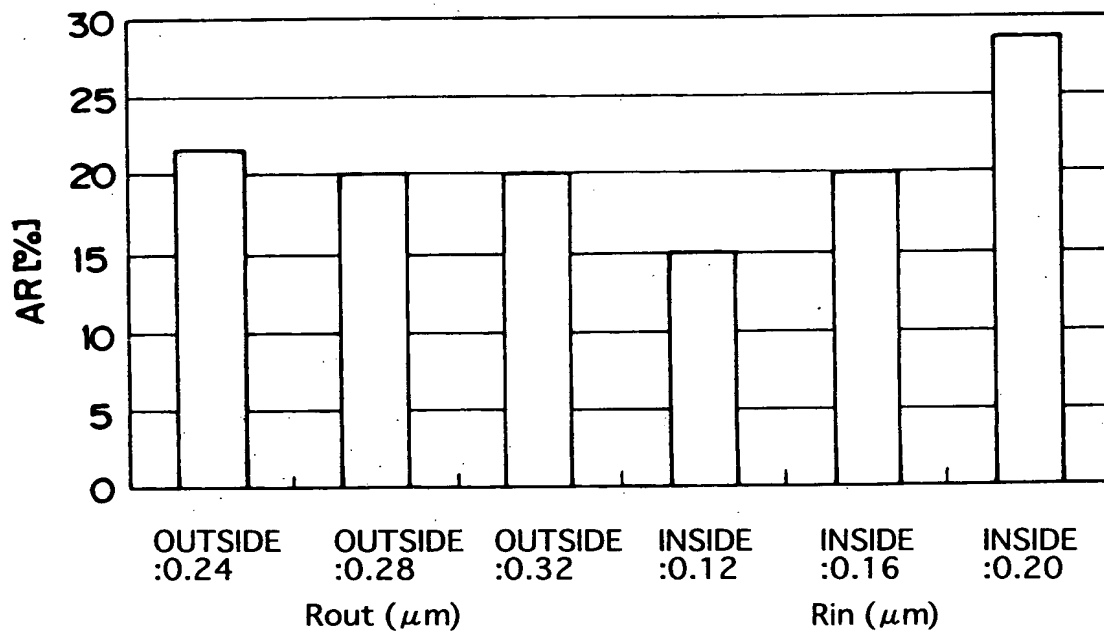


Fig.12

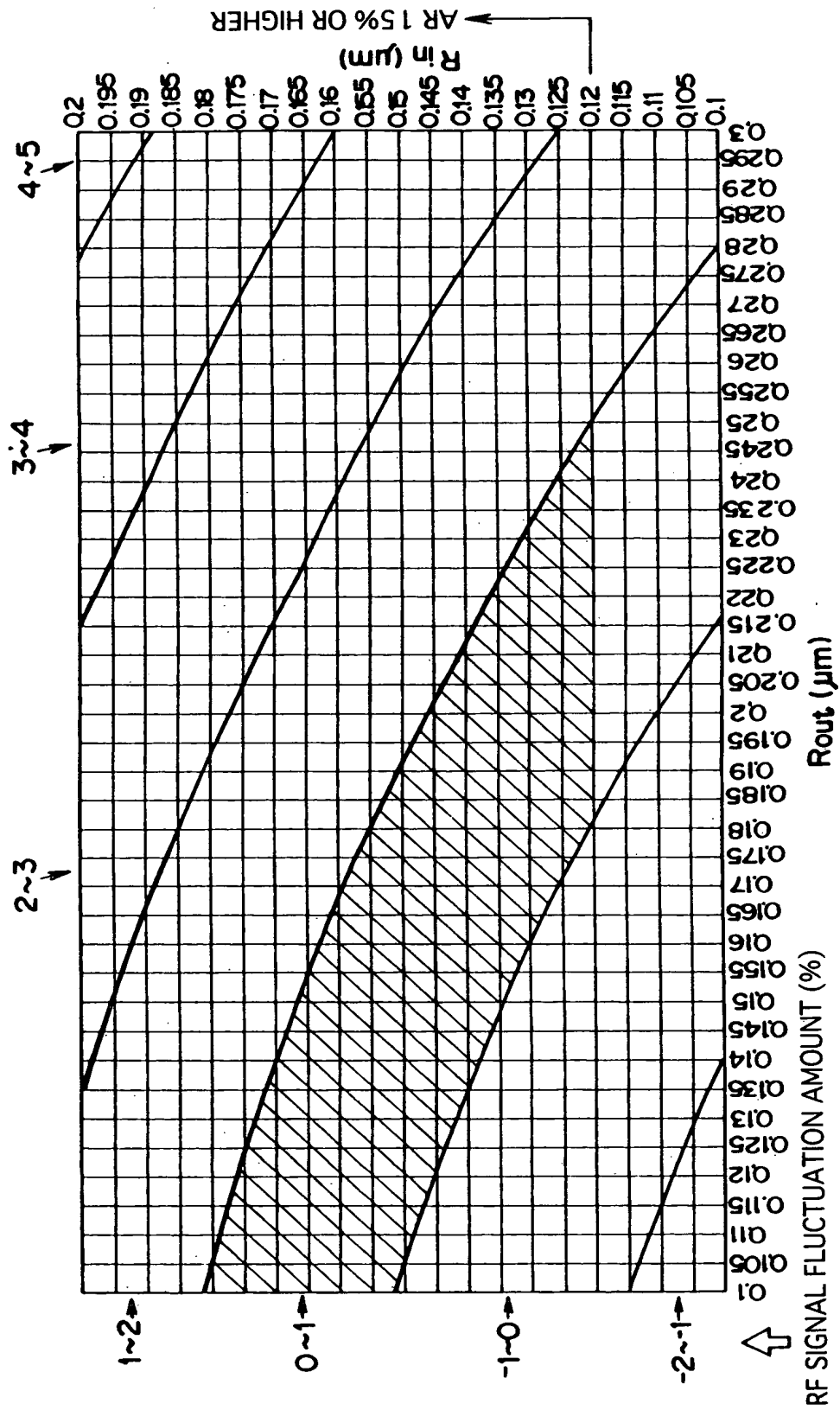


Fig. 13

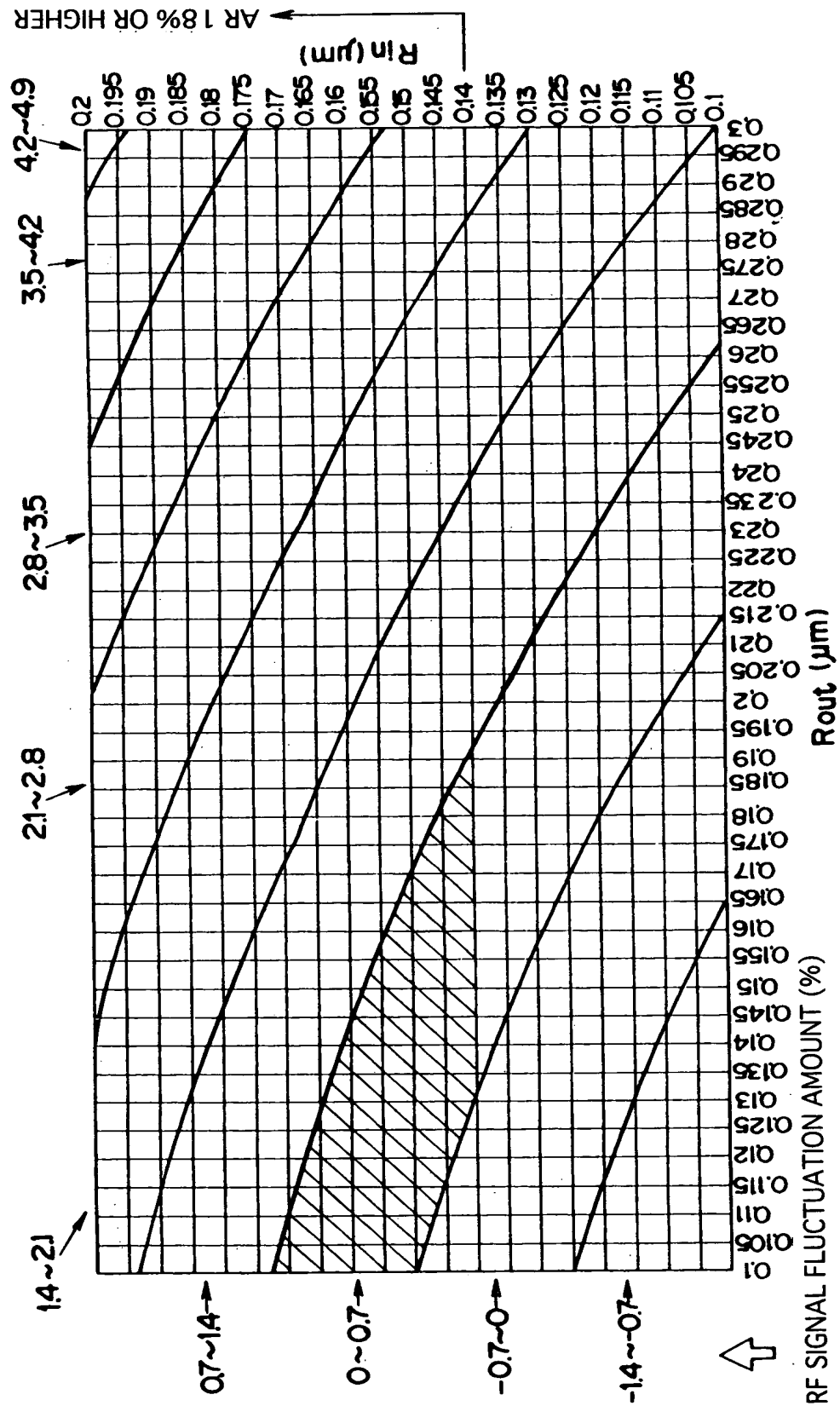


Fig.14

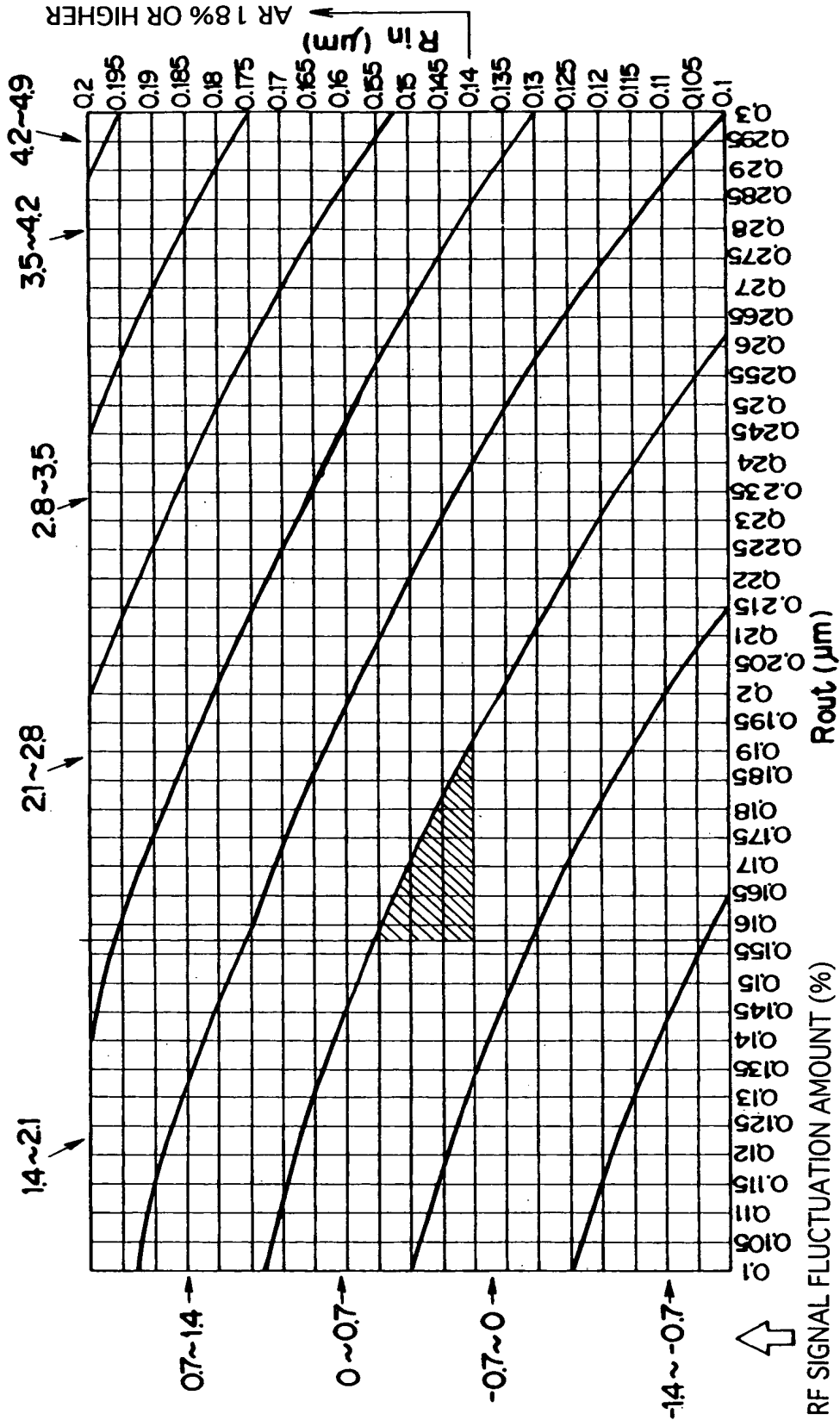




Fig.15  
PRIOR ART

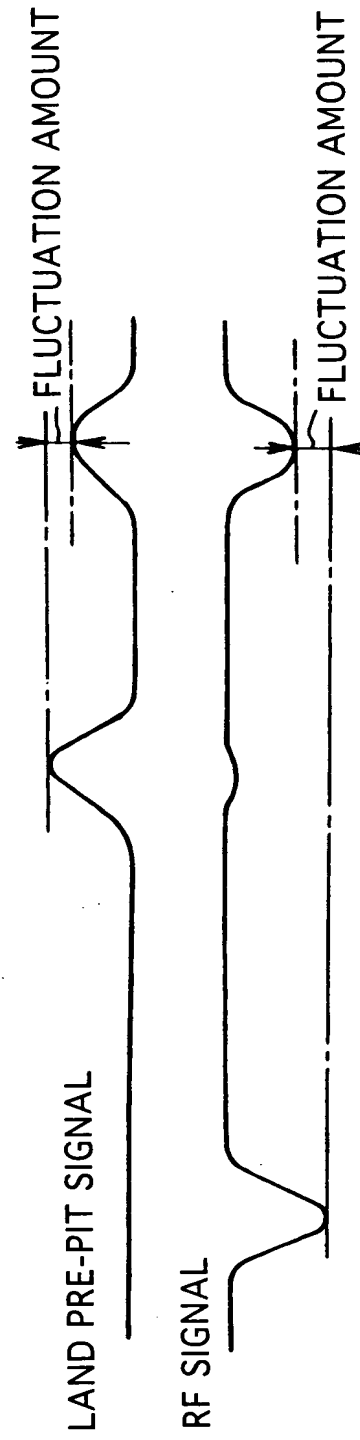
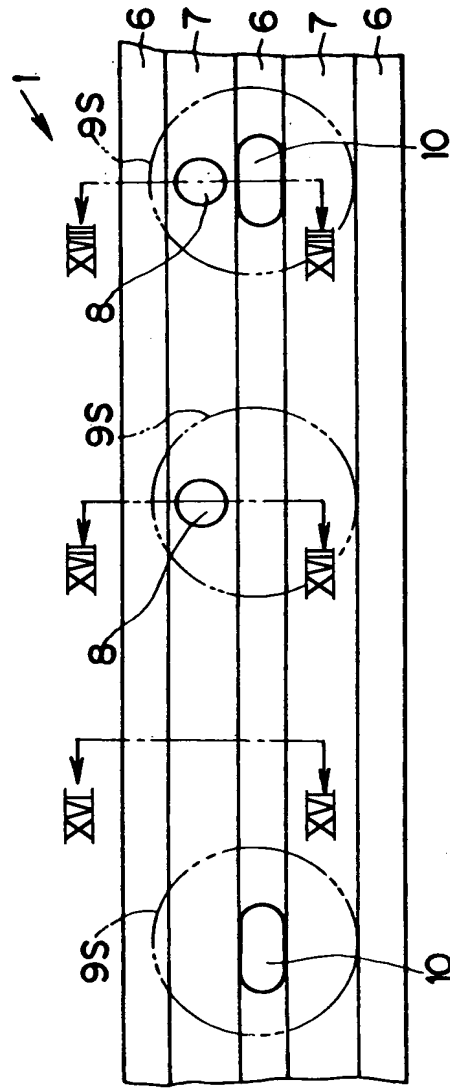


Fig.16  
PRIOR ART

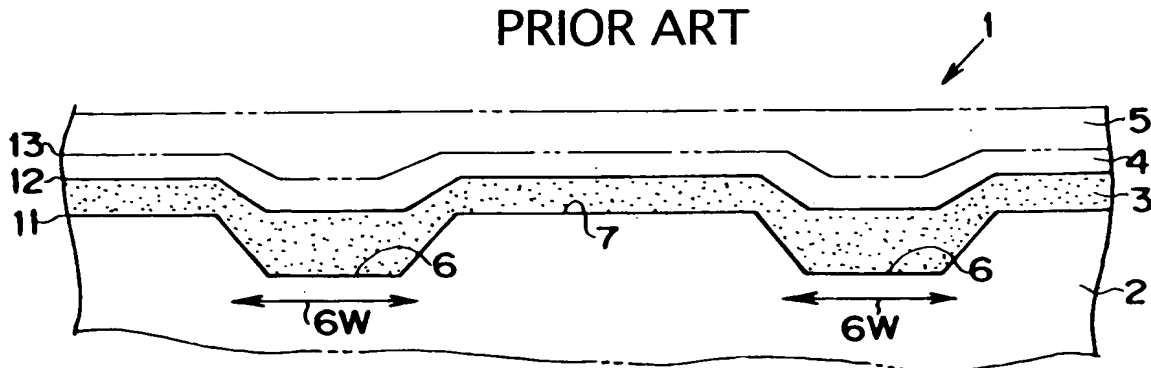


Fig.17  
PRIOR ART

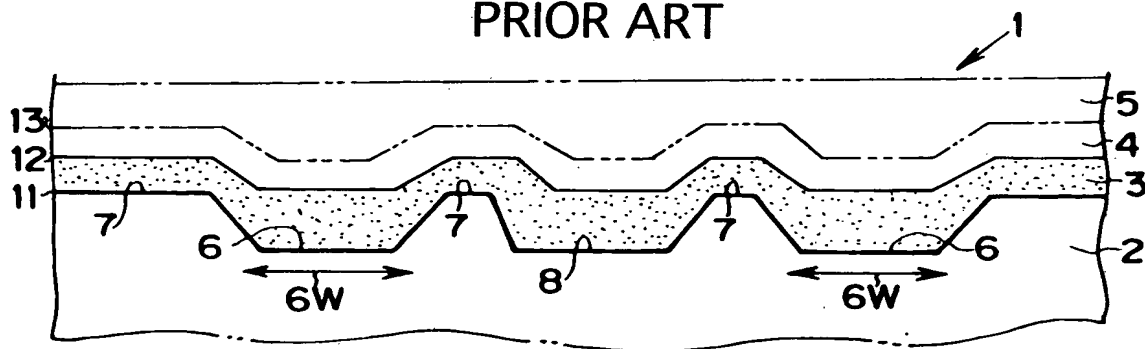


Fig.18  
PRIOR ART

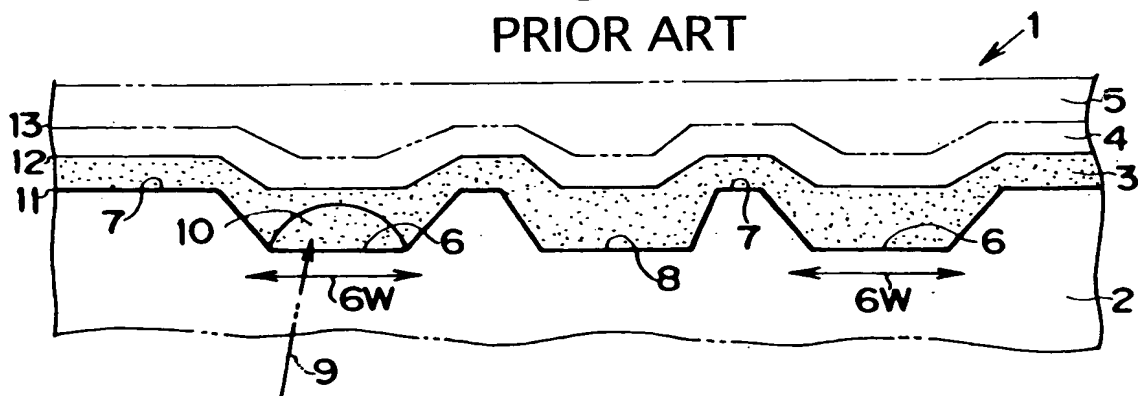


Fig.19  
PRIOR ART

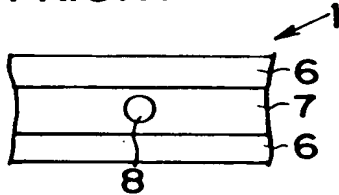


Fig.20  
PRIOR ART

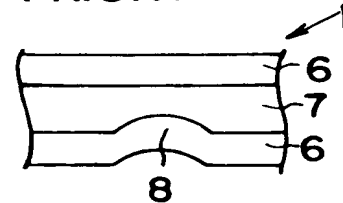


Fig.21  
PRIOR ART

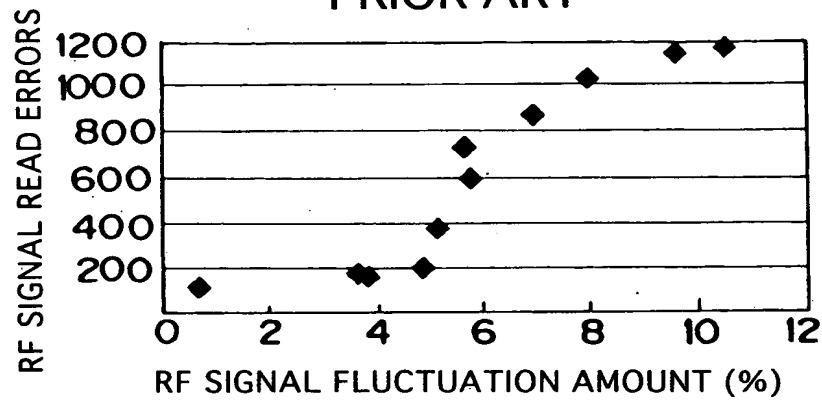


Fig.22  
PRIOR ART

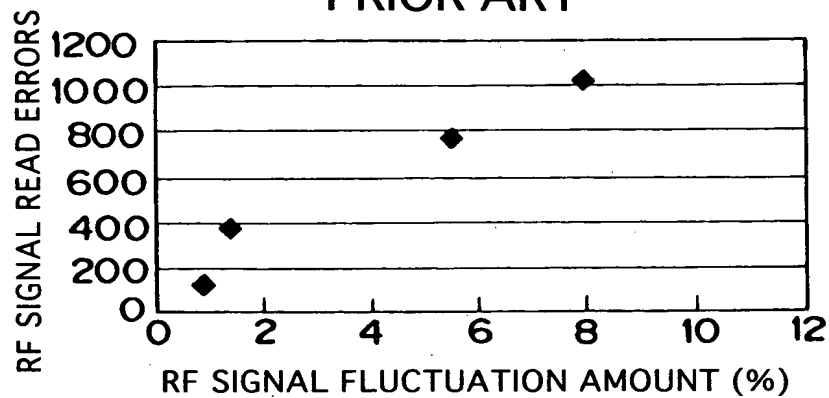


Fig.23  
PRIOR ART

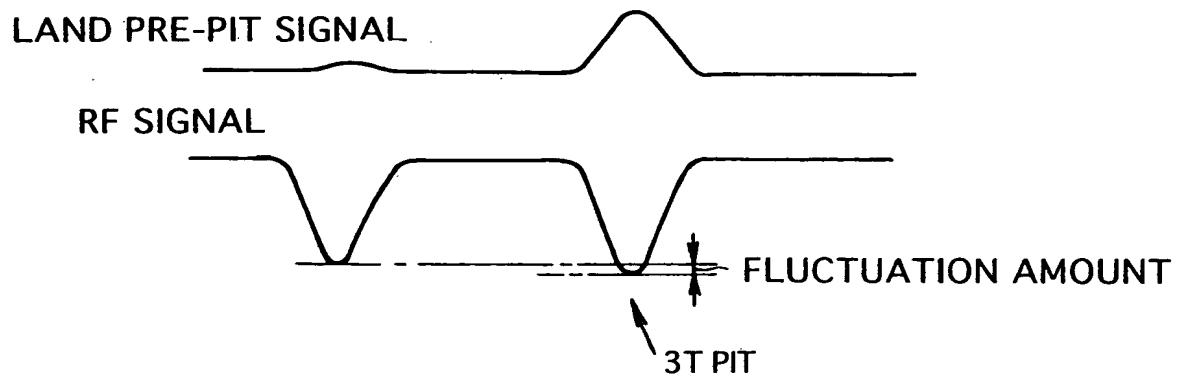


Fig.24  
PRIOR ART

